



## DIGITAL VERIFICATION ENGINEER (m/f/d)

Inova Semiconductors a Munich-based fabless semiconductor manufacturer founded in 1999, designs, markets and sells its products and licensed technologies both directly and through a global network of distributors, heavily focusing on the automotive market. The core competence of Inova resides in the development and marketing of high-speed digital data transmission semiconductor technologies for harsh automotive environments.

APIX™, APIX™2 and APIX™3 are the company's flagship product lines that have delivered major advancements in digital multimedia transmission, particularly in the automotive, industrial and transportation market segments. Inova Semiconductors has recently entered the emerging smart digital LED market under the ISELED™ brand name as member of an open industry-wide alliance.

### JOB SUMMARY

Participation in the Inova digital design flow including:

- Specification of new APIX devices and components
- Enhancement of our UVM system level verification environment to support new features
- Definition and implementation of module level verification environments for new components of the APIX system
- Generation of verification plans in close cooperation with the design team
- Implementation of constrained random and directed test cases in the APIX UVM verification environment and analysis of simulation results
- Development of assertion based verification components
- Support for operation start-up of new devices in our lab environment

### KEY QUALIFICATIONS

- Degree in electrical engineering / computer science or a similar field of study
- Experience in digital verification (System Verilog / UVM) is required
- Experience with constrained random verification environments is a plus
- Experience in digital design (System Verilog / Verilog) is a plus
- Good analytical skill set
- Solid Linux skills and experience in scripting languages (Python) are a plus
- Team-oriented style of work
- Good communication skills, fluent in German and English
- 3+ years of professional experience in digital design and verification

### CONTACT

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