



DIGITAL DESIGN ENGINEER

Inova Semiconductors a Munich-based fabless semiconductor manufacturer founded in 1999, designs, markets and sells its products and licensed technologies both directly and through a global network of distributors, heavily focusing on the automotive market. The core competence of Inova resides in the development and marketing of high-speed digital data transmission semiconductor technologies for harsh automotive environments.

APIX™, APIX™2 and APIX™3 are the company's flagship product lines that have delivered major advancements in digital multimedia transmission, particularly in the automotive, industrial and transportation market segments. Inova Semiconductors has recently entered the emerging smart digital LED market under the ISELED™ brand name as member of an open industry-wide alliance.

JOB SUMMARY

- Design & Verification of digital blocks and toplevel for highspeed serial link circuits for automotive infotainment devices.

DESCRIPTION

- Work with systems team to understand the top level requirements of the digital functions and develop detailed specifications.
- Implement the function in Verilog and System Verilog RTL specification.
- Other responsibilities could include RTL synthesis.

CONTACT

Inova Semiconductors GmbH
Human Resources
Grafinger Str. 26
D-81671 Munich - Germany
Phone: +49-89-457475-60
Fax: +49-89-457475-88
hr@inova-semiconductors.de
www.inova-semiconductors.de

KEY QUALIFICATIONS

- Experience in digital design including RTL design, with System Verilog.
- Experience in digital verification, with System Verilog.
- Understanding of digital design flow including RTL simulation and logic synthesis.
- Understanding of design verification.
- Knowledge of best practices with respect to verification of digital logic.
- Good written and verbal communication skills.
- Experience in working with international teams.
- Fluent in German and English.